



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/803,203 Confirmation No.: 5841
First Named Inventor: Bulucea, Constantin Filing Date: 17 March 2004
Group Art Unit: 2826 Examiner: Quach, T. N.
Atty. Docket No.: NS-5737 US
Title: Configuration and Fabrication of Semiconductor Structure Having
N-channel Channel-junction Field-effect Transistor
Assignee(s): National Semiconductor Corporation

Mountain View, California
8 December 2006

**MAIL STOP ISSUE FEE
COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, Virginia 22313-1450**

SUBMISSION OF REPLACEMENT DRAWINGS

Sir:

Enclosed for the above patent application are twenty-four sheets of replacement drawings consisting of Figs. 1 - 14f. The enclosed drawings incorporate both the previously approved revisions and the revisions requested in the accompanying Amendment to Drawings. If the revisions in the accompanying Amendment to Drawings are acceptable, please substitute the enclosed drawings for the informal drawings now on file.

Please telephone Attorney for Applicant(s) at 650-964-9767 if there are any questions regarding these drawings.

EXPRESS MAIL LABEL NO.:

EV 743 585 428 US

Respectfully submitted,

Ronald J. Meetin
Attorney for Applicant(s)
Reg. No. 29,089

210 Central Avenue
Mountain View, CA 94043-4869

Ronald J. Meetin
Attorney at Law
210 Central Avenue
Mountain View, CA
94043-4869
Tel.: 650-964-9767
Fax: 650-964-9779